

## PATENT ABSTRACTS OF JAPAN

(11)Publication number : 09-160684  
 (43)Date of publication of application : 20.06.1997

(51)Int.CI.

G06F 1/32

(21)Application number : 07-321621  
 (22)Date of filing : 11.12.1995

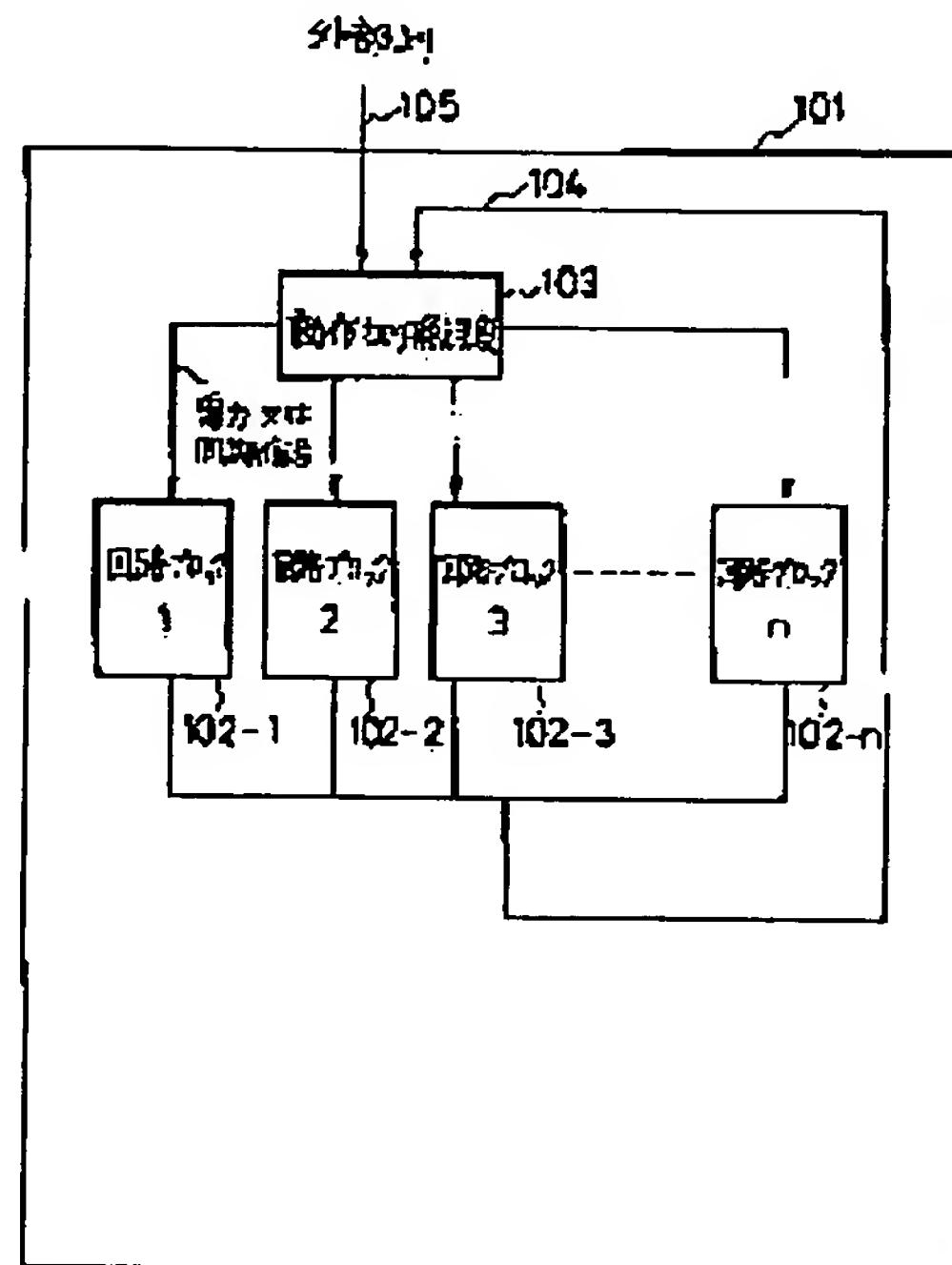
(71)Applicant : FUJI ELECTRIC CO LTD  
 (72)Inventor : SUZUKI TAKAHIRO  
 KANZAKI NOBORU  
 YOSHIDA YUTAKA

## (54) LOW CONSUMED POWER DEVICE

## (57)Abstract:

**PROBLEM TO BE SOLVED:** To lower the power consumed in a device which is operated by limited electric power from a battery, etc.

**SOLUTION:** Devices 101 are divided into circuit blocks 102-1 to 102-n by operation functions, and the operation of a circuit block which wasting electric power by performing unnecessary operation is stopped by connecting and disconnecting the electric power or synchronizing signal placing the circuit blocks 102-1 to 102-n in operation, block by block, to reduce the consumed power in the whole device. Further, when a transmitted frame is received, a device for transmission compares its station address with address information on the received frame and actuates a controller such as a microcomputer only when the received frame is addressed to its station to enter a reception state, but automatically cancels the frame without actuating the controller such as the microcomputer if the received frame is a transmitted frame addressed to another station, so that the consumed power in the whole system is reduced as a result.



## LEGAL STATUS

[Date of request for examination]

[Date of sending the examiner's decision of rejection]

[Kind of final disposal of application other than the examiner's decision of rejection or application converted registration]

[Date of final disposal for application]

[Patent number]

[Date of registration]

[Number of appeal against examiner's decision of rejection]